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U.S. UTILITY Patent Application

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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2001-044949 02/21/2001

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Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners initials	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO Q68480	
TITLE : Manufacturing method of semiconductor device			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	<input type="checkbox"/> Sheets Drwg.	<input type="checkbox"/> Figs.Drwg.	<input type="checkbox"/> Print Fig.
		Primary Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner
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